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Dr. Fabio Somenzi is a professor in the Electrical, Computer & Energy Engineering department at the University of Colorado Boulder. His research interests are in the Computer Engineering Research Area in formal verification, model checking, decision procedures, et cetera.

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In 1987 he visited the Electrical Engineering and Computer Science Department of the University of California, Berkeley. Since 1989 he has been an with the Department of Electrical and Computer Engineering of the University of Colorado, Boulder, where he is currently a Full Professor. Personal Data. Born in Salerno, Italy, on September 17, 1957.

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"Methods for Timing Analysis and Logic Synthesis to Decrease Power Dissipation of VLSI Circuits," 1995.

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He is currently pursuing the Ph.D degree from the Department of Electrical and Computer Engineering at the University of Colorado at Boulder. His research interests include formal methods in computer-aided-design and model checking. He is currently working on algorithmic improvements to inductive model checkers.

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