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## Computation Structures Mit Electrical Engineering

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This course introduces architecture of digital systems, emphasizing structural principles common to a wide range of technologies. It covers the topics including multilevel implementation strategies, definition of new primitives (e.g., gates, instructions, procedures, processes) and their mechanization using lower-level elements. It also includes analysis of potential concurrency, precedence ...

## **Computation Structures | Electrical Engineering and ...**

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Developed as the text for the basic computer architecture course at MIT, Computation Structures integrates a thorough coverage of digital logic design with a comprehensive presentation of computer architecture. It contains a wealth of information for those who design computers or work with computer systems, spanning the entire range of topics from analog circuit design to operating systems.

## **Computation Structures (MIT Electrical Engineering and**

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Starting with MOS transistors, the course develops a series of building blocks — logic gates, combinational and sequential circuits, finite-state machines, computers and finally complete systems. Both hardware and software mechanisms are explored through a series of design examples.

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Feedback, comments, and errata are welcome; please direct them to [6004x-notes-feedback@csail.mit.edu](mailto:6004x-notes-feedback@csail.mit.edu) . Computation Structures is an introductory course about the design and implementation of digital systems, emphasizing structural principles common to a wide range of technologies. Starting at the device level, the course develops a hierarchical set of building blocks — logic gates, combinational and sequential circuits, finite-state machines, processors and finally complete systems.

## **[computationstructures.org](http://computationstructures.org)**

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operating systems.

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## **Labs | Computation Structures | Electrical Engineering and ...**

This course makes use of Athena, MIT's UNIX-based computing environment. OCW does not provide access to this environment. Course Meeting Times. Lectures: 2 sessions / week, 1 hour / session. Recitations: 2 sessions / week, 1 hour / session. Course Description. 6.004 offers an introduction to the engineering of digital systems.

## **Syllabus | Computation Structures | Electrical Engineering**

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## **Calendar | Computation Structures | Electrical Engineering ...**

Computation Structures 6.004 Cellular Neurophysiology 6.021  
Nanoelectronics 6.012 Signals 6.003 Electromagnetic Fields  
6.014 Circuits 6.002 Signals and Systems 1 6.011 students  
choose three header subjects, which typically rely on a  
foundation course as a pre-requisite foundation subjects build on  
introductory material introductory subjects introduce

## **6-1: Electrical Science and Engineering**

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Institute of Technology. Chris ...

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The Institute's largest academic department reorganizes with new leadership as part of the formation of the MIT Schwarzman College of Computing. As part of the founding of the MIT Stephen A. Schwarzman College of Computing, the Department of Electrical Engineering and Computer Science (EECS), the largest academic unit at MIT, has been restructured to provide a stronger base for enhancing existing programs, creating new opportunities, and increasing connections to other parts of the Institute.

## **Restructuring the MIT Department of Electrical Engineering ...**

Stephen A. Ward, Robert H. Halstead. MIT Press, 1990 - Computers - 789 pages. 2 Reviews. Developed as the text for the



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basic computer architecture course at MIT, ComputationStructures integrates a...

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ELECTRICAL ENGINEERING AND COMPUTER SCIENCE (COURSE 6)  
6.006 Introduction to Algorithms Prereq: 6.042[J] and (6.0001 or Coreq: 6.009) U (Fall, Spring) 4-0-8 units Introduction to mathematical modeling of computational problems, as well as common algorithms, algorithmic paradigms, and data structures used to solve these problems. Emphasizes the

## **Electrical Engineering and Computer Science (Course 6) -**

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## **Meet the Educator | Instructor Insights | Computation ...**

Srini Devadas, the Edwin Sibley Webster Professor in MIT's Electrical Engineering and Computer Science Department and members of his group, the Computation Structures Group, have designed a process for thwarting memory-access attacks to steal

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data. Their scheme includes custom-built reconfigurable chips, now moving into fabrication.

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